# Exhibit 4

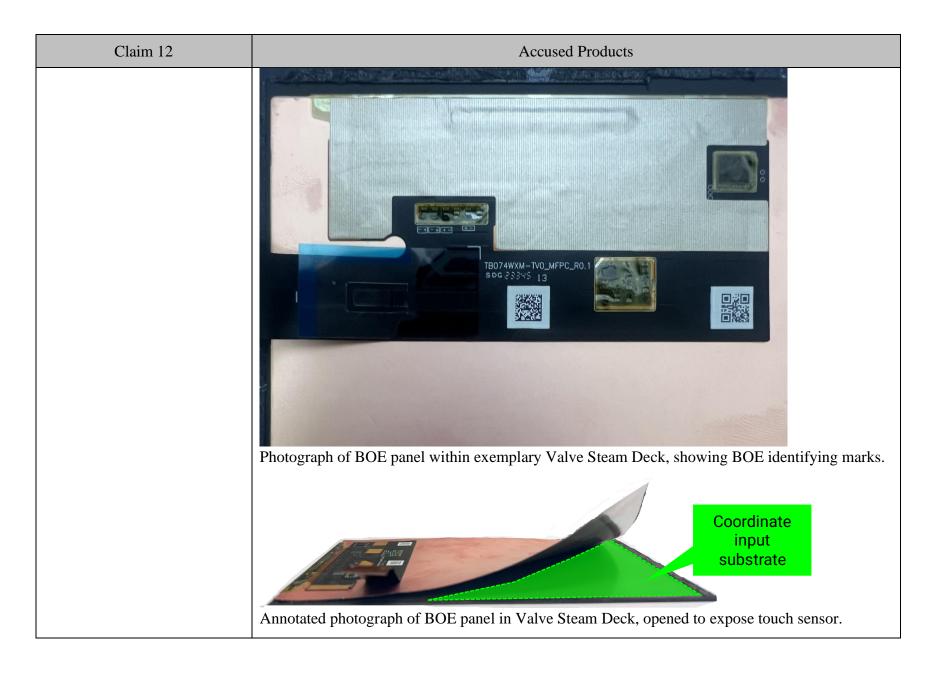
### <u>U.S. Patent No. 9,507,477 ("'477 Patent")</u>

#### **Accused Products**

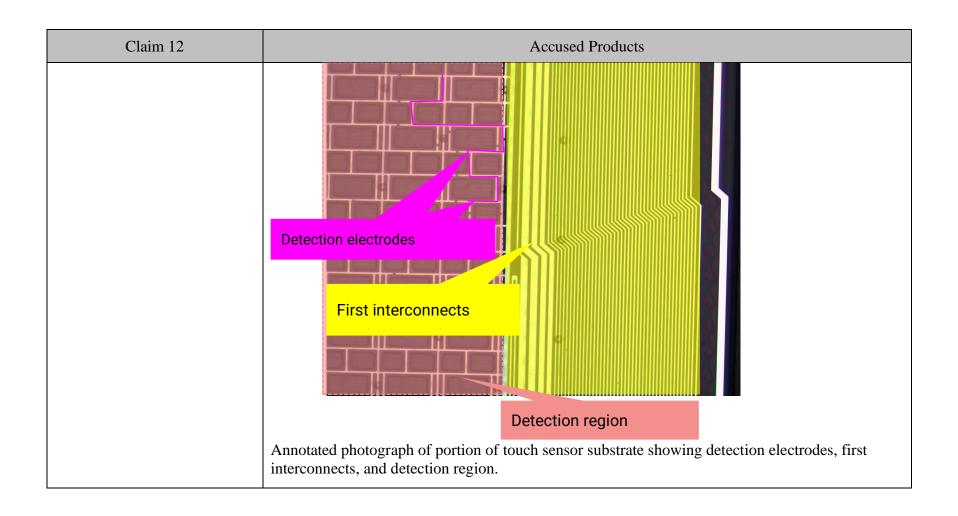
BOE products, including without limitation the BOE panel with touch sensor in the Valve Steam Deck, and all versions and variations thereof since the issuance of the asserted patent.

#### Claim 12

Claim 12	Accused Products
[12.0] A coordinate input device comprising:	To the extent the preamble is limiting, each Accused Product is or comprises a coordinate input device.  See discussion of claim limitations below.  See also, e.g.:
	Photograph of exemplary Valve Steam Deck containing BOE panel.

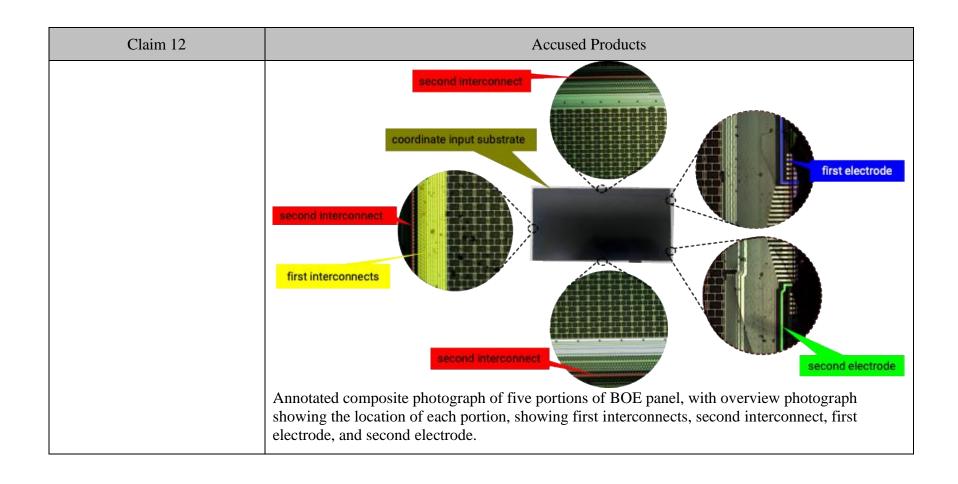


Claim 12	Accused Products
[12.1] a coordinate input substrate having detection electrodes disposed in a detection region, and first interconnects outside the detection region; and	Each Accused Product comprises a coordinate input substrate having detection electrodes disposed in a detection region, and first interconnects outside the detection region.  See, e.g.:  Coordinate input substrate  input substrate  Annotated photograph of BOE panel in Valve Steam Deck, opened to expose touch sensor substrate.



Claim 12	Accused Products
[12.2] a first flexible substrate electrically connected to the coordinate input substrate,	Each Accused Product comprises a first flexible substrate electrically connected to the coordinate input substrate.  See, e.g.:
	Coordinate input substrate  Annotated photograph of portion of touch sensor substrate showing first flexible substrate electrically connected to touch sensor substrate (behind shield).

Claim 12	Accused Products
	Coordinate input substrate  Annotated photograph of BOE panel in Valve Steam Deck, opened to expose touch sensor substrate.
[12.3] wherein the coordinate input substrate has a second interconnect, a first electrode, and a second electrode,	In each Accused Product, the coordinate input substrate has a second interconnect, a first electrode, and a second electrode.  See, e.g.:



Claim 12	Accused Products
Claim 12 [12.4] wherein the second interconnect surrounds the detection region and is formed outside the first interconnects,	In each Accused Product, the second interconnect surrounds the detection region and is formed outside the first interconnects.  See, e.g.:  Second interconnect  Surrounds  detection region  Second interconnect
	Annotated composite photograph of five portions of BOE panel, with overview photograph showing the location of each portion, showing second interconnect surrounding the detection region.

Claim 12	Accused Products
[12.5] wherein the first electrode is formed at one end of the second interconnect,	In each Accused Product, the first electrode is formed at one end of the second interconnect.  See, e.g.:
	first electrode  one end
	second interconnect
	Annotated composite photograph of four portions of BOE panel, with overview photograph showing the location of each portion, showing first electrode formed at one end of the second interconnect.

Claim 12	Accused Products
[12.6] wherein the second electrode is formed at the other end of the second interconnect,	In each Accused Product, the second electrode is formed at the other end of the second interconnect.  See, e.g.:
	second interconnect
	the other end  second interconnect  second electrode
	Annotated composite photograph of four portions of BOE panel, with overview photograph showing the location of each portion, showing second electrode formed at the other end of the second interconnect.

Claim 12	Accused Products
Claim 12  [12.7] wherein the second interconnect is disposed along a peripheral edge of at least three sides of the coordinate input substrate, and	Accused Products  In each Accused Product, the second interconnect is disposed along a peripheral edge of at least three sides of the coordinate input substrate.  See, e.g.:  coordinate input substrate  second interconnect  three sides
	Annotated composite photograph of three portions of BOE panel, with overview photograph showing the location of each portion, showing second interconnect disposed along a peripheral edge of at least three sides of the coordinate input substrate.

## Claim 12 **Accused Products** [12.8] wherein the first In each Accused Product, the first electrode is electrically connected with a third electrode on the electrode is electrically first flexible substrate, or the second electrode is electrically connected with a fourth electrode on connected with a third the first flexible substrate. electrode on the first flexible *See, e.g.*: substrate, or the second electrode is electrically electrically connected first electrode connected with a fourth electrode on the first flexible third electrode substrate. first flexible substrate fourth electrode second electrode Annotated composite photograph of four portions of BOE panel, with overview photograph showing the location of each portion, showing first electrode, second electrode, third electrode on first flexible substrate, fourth electrode on first flexible substrate, and electrical connections between first and third electrodes and second and fourth electrodes respectively.